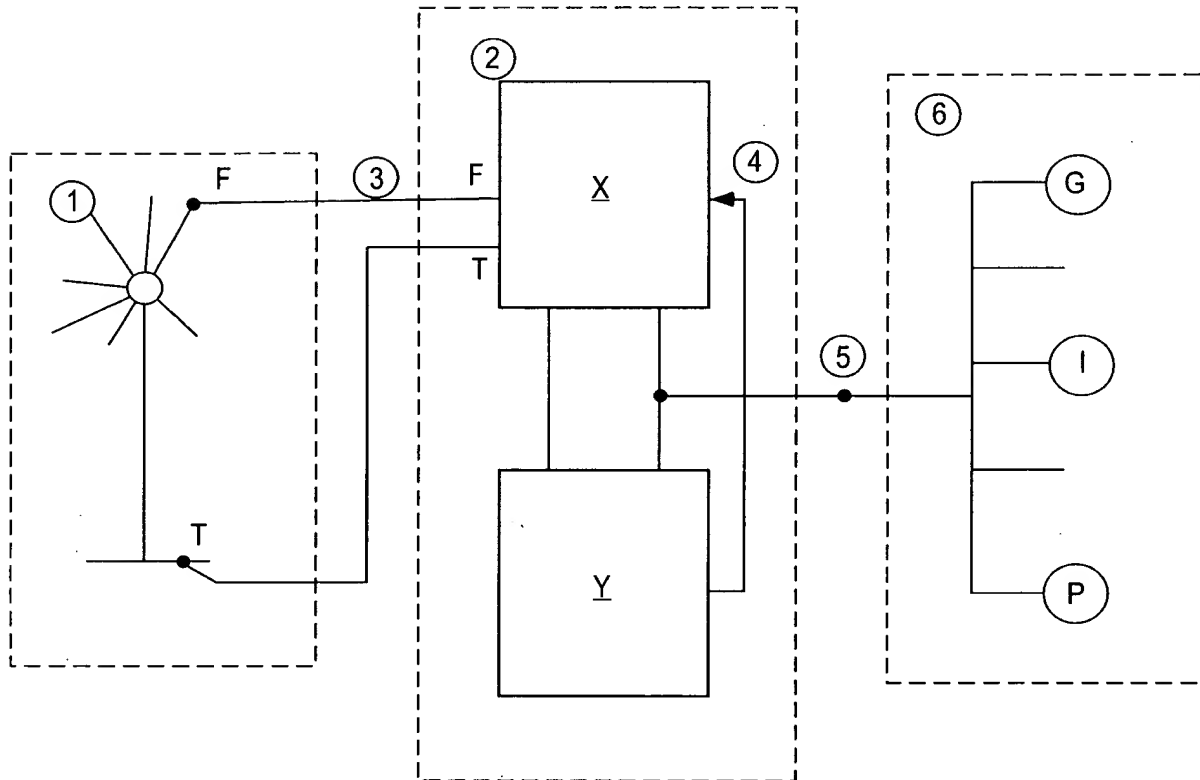


1/10

FIG. 1

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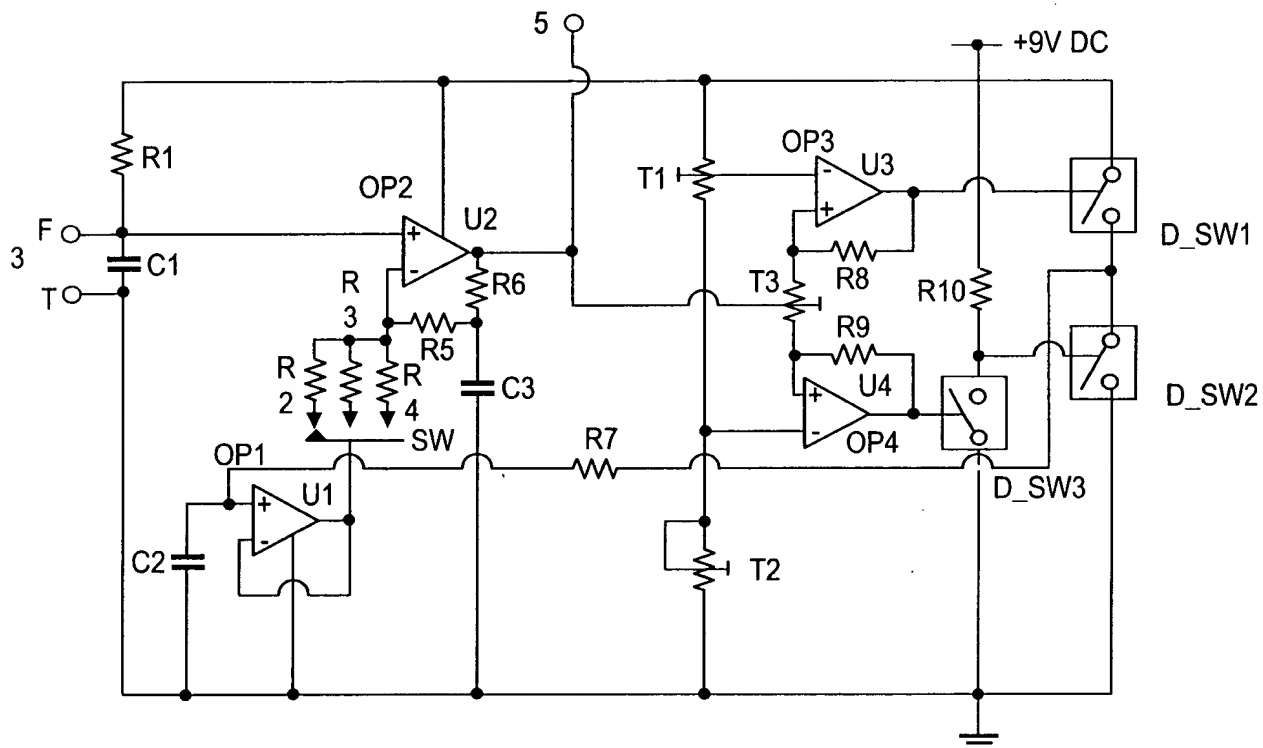


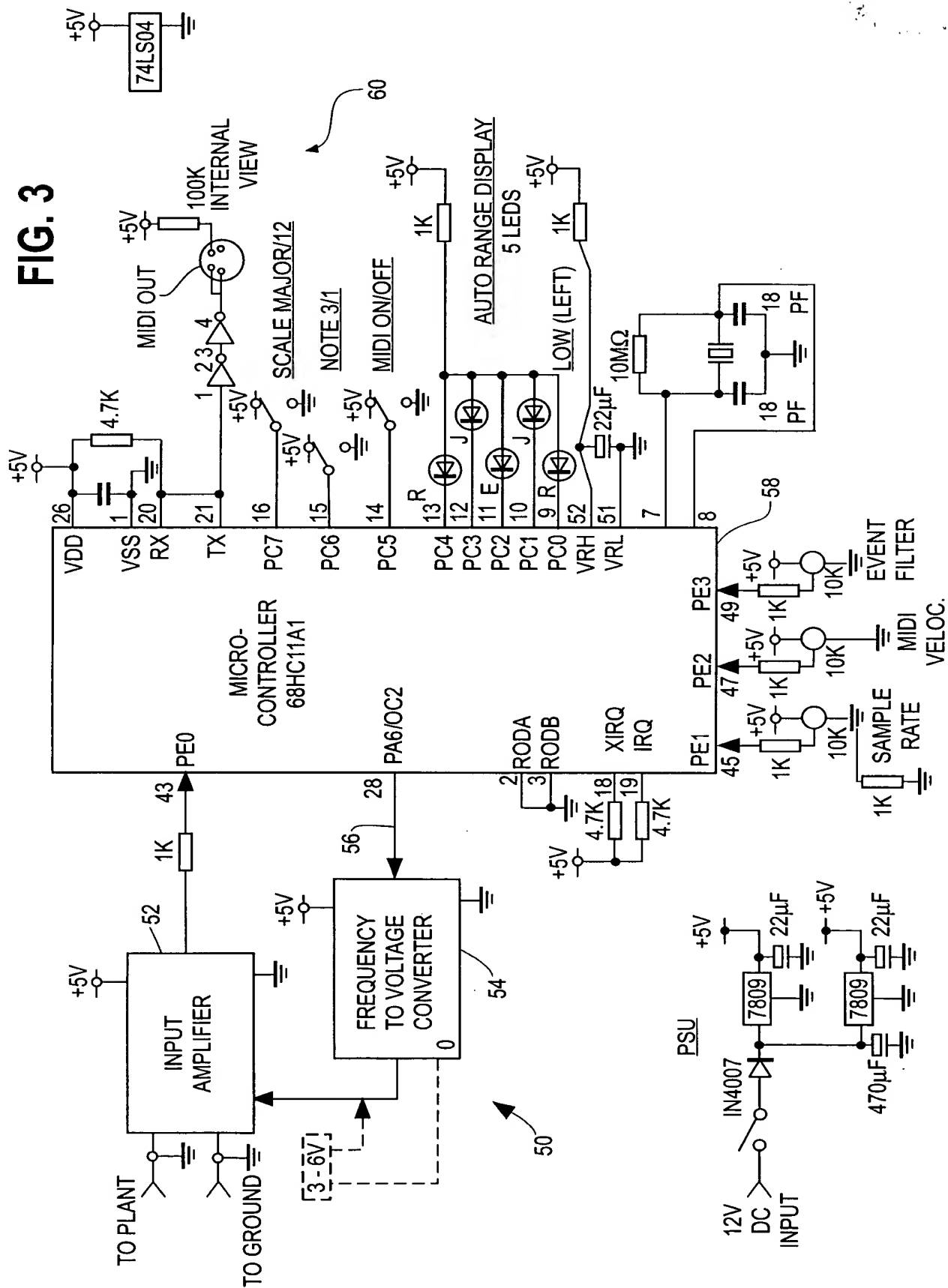
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2/10

FIG. 2

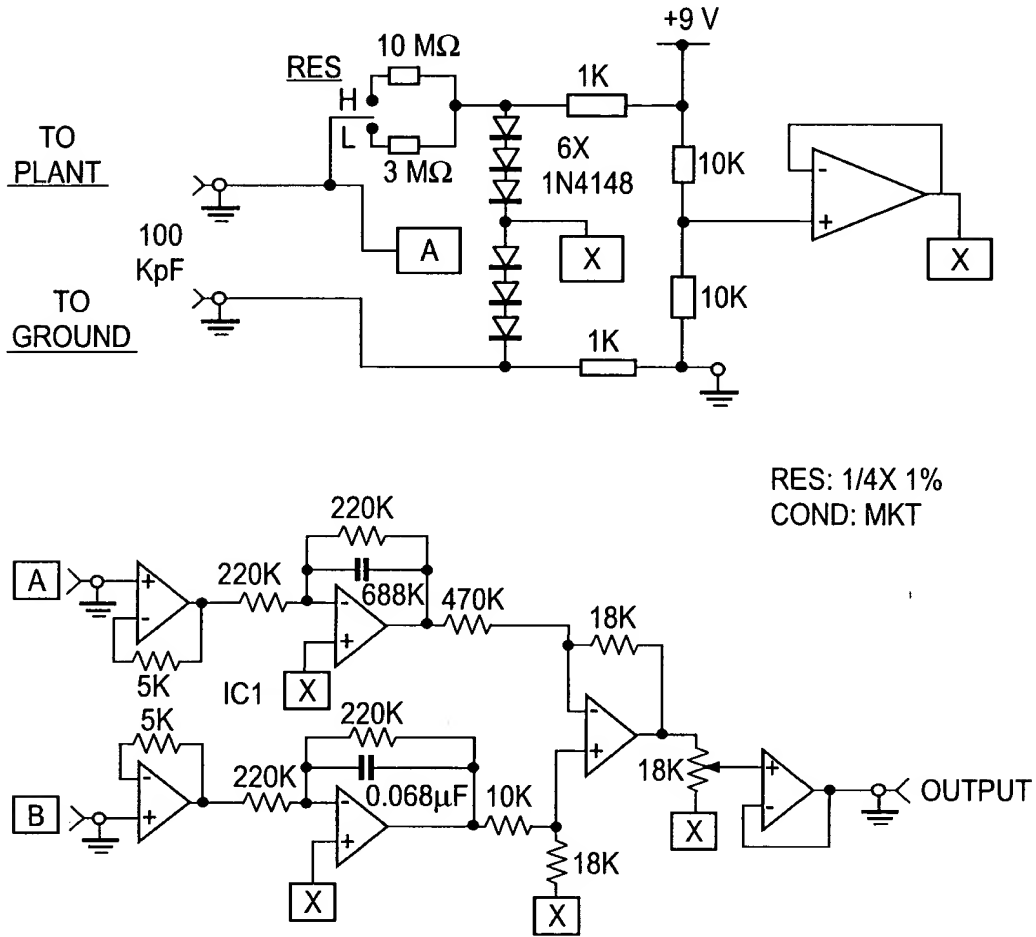


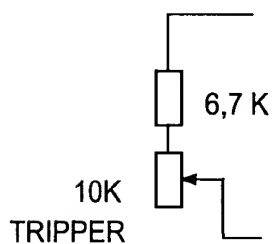


4/10

FIG. 4

INPUT AMPLIFIER





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FIG. 6

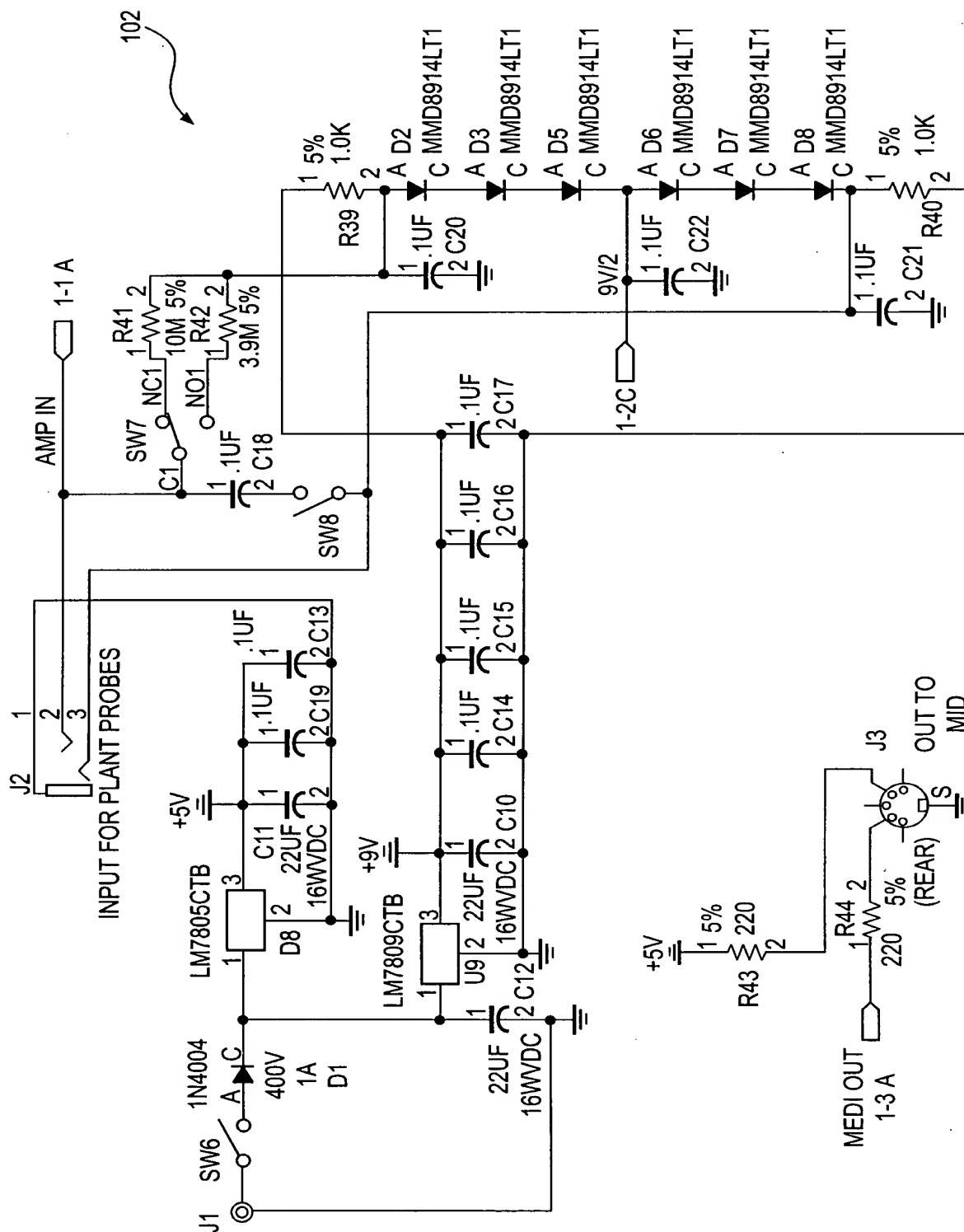


FIG. 7

7/10

FIG. 7a	FIG. 7b	FIG. 7c
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FIG. 7a

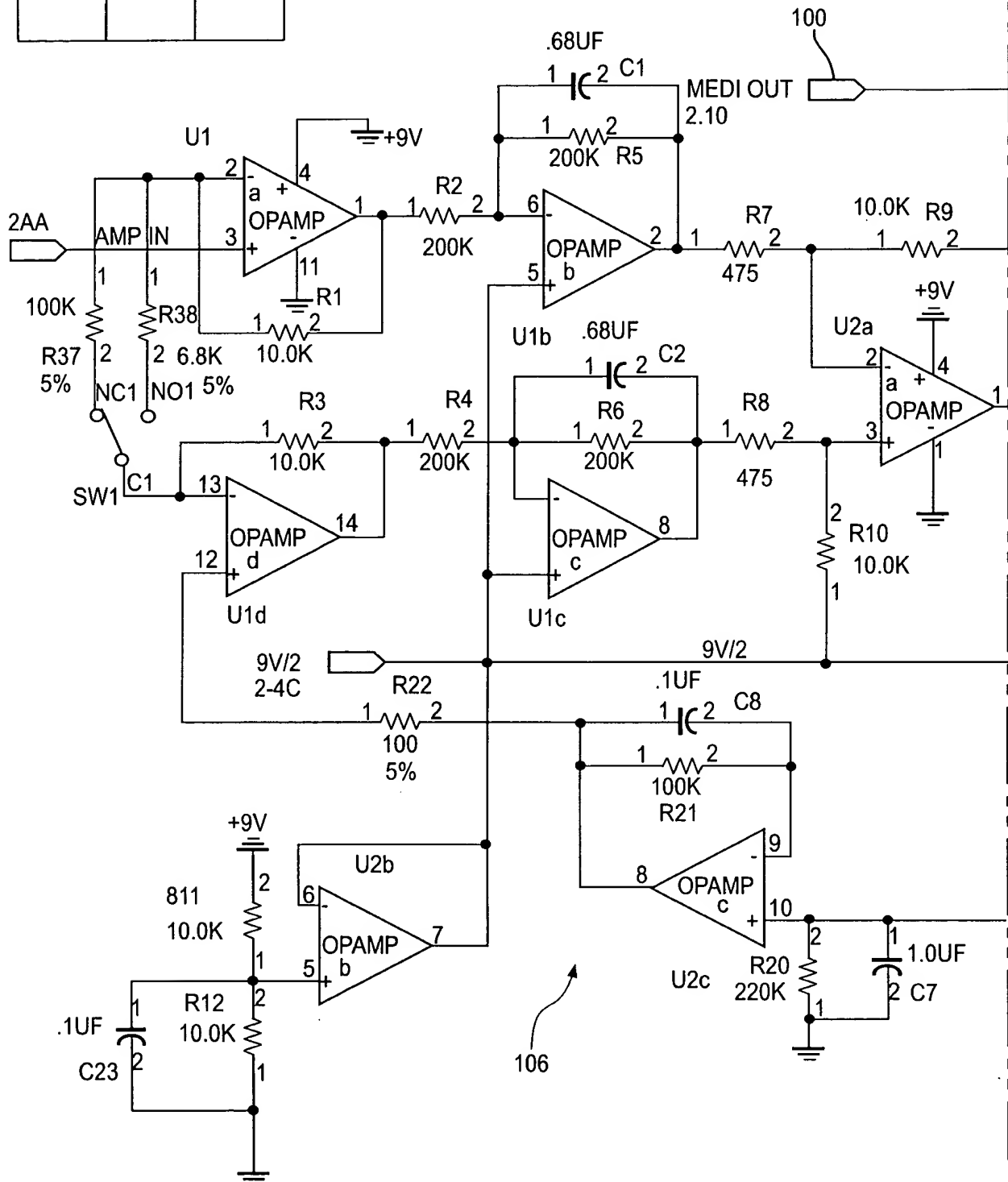
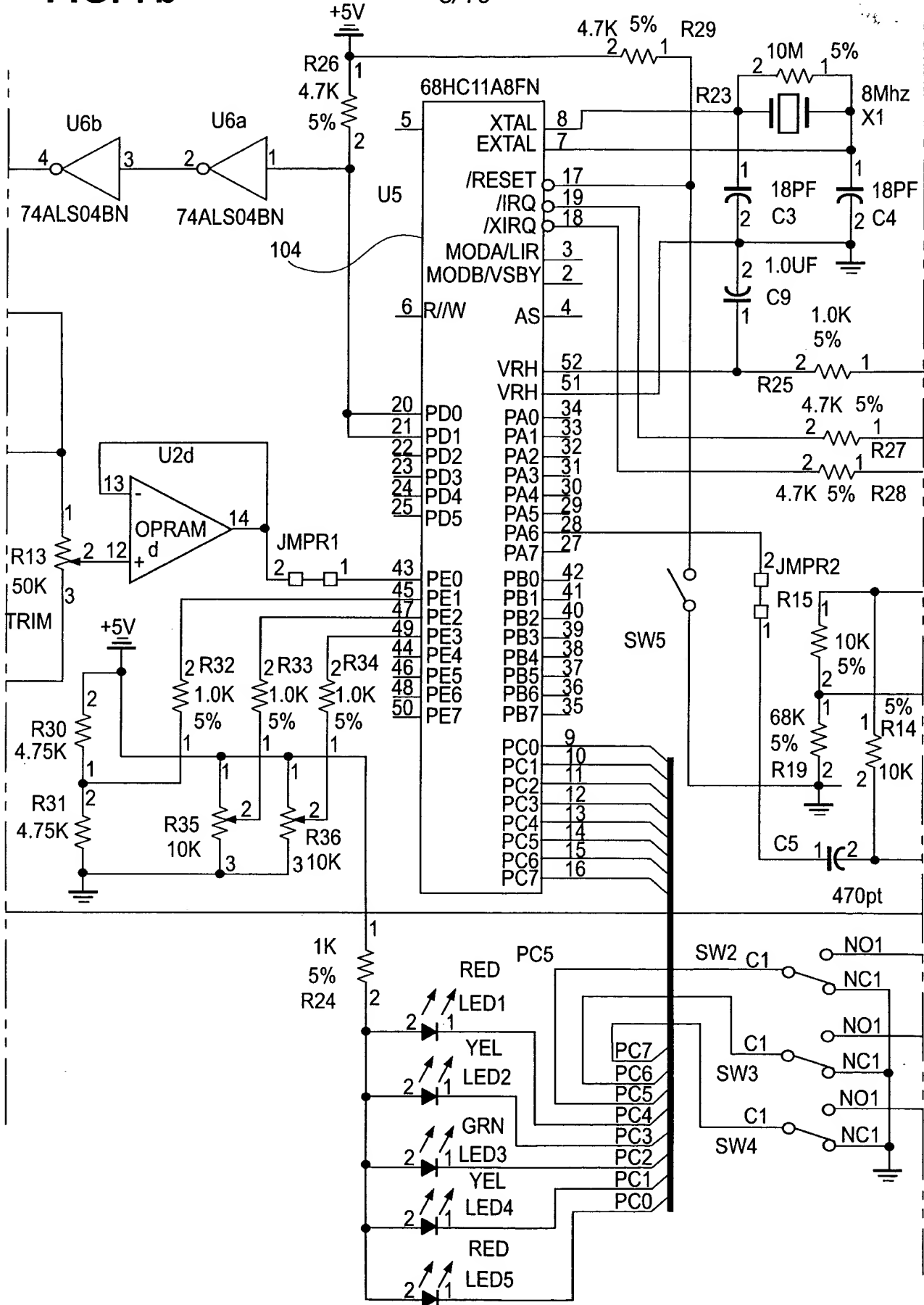


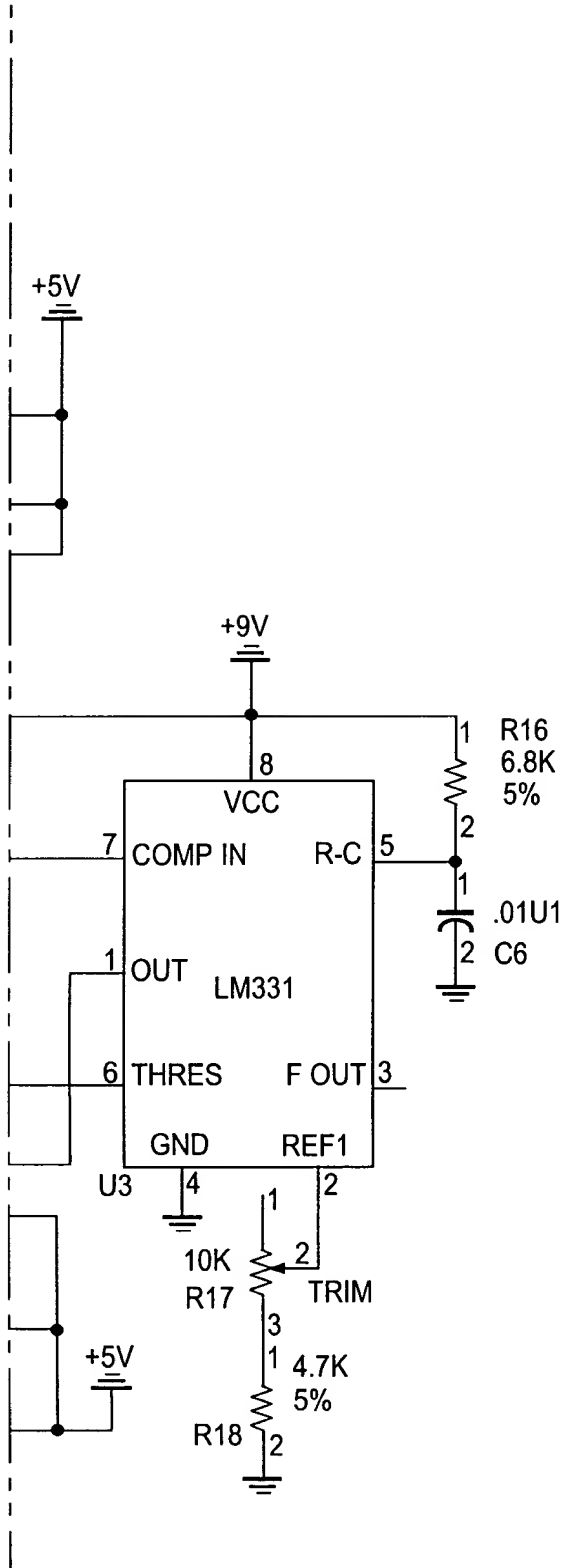
FIG. 7b

8/10



9/10

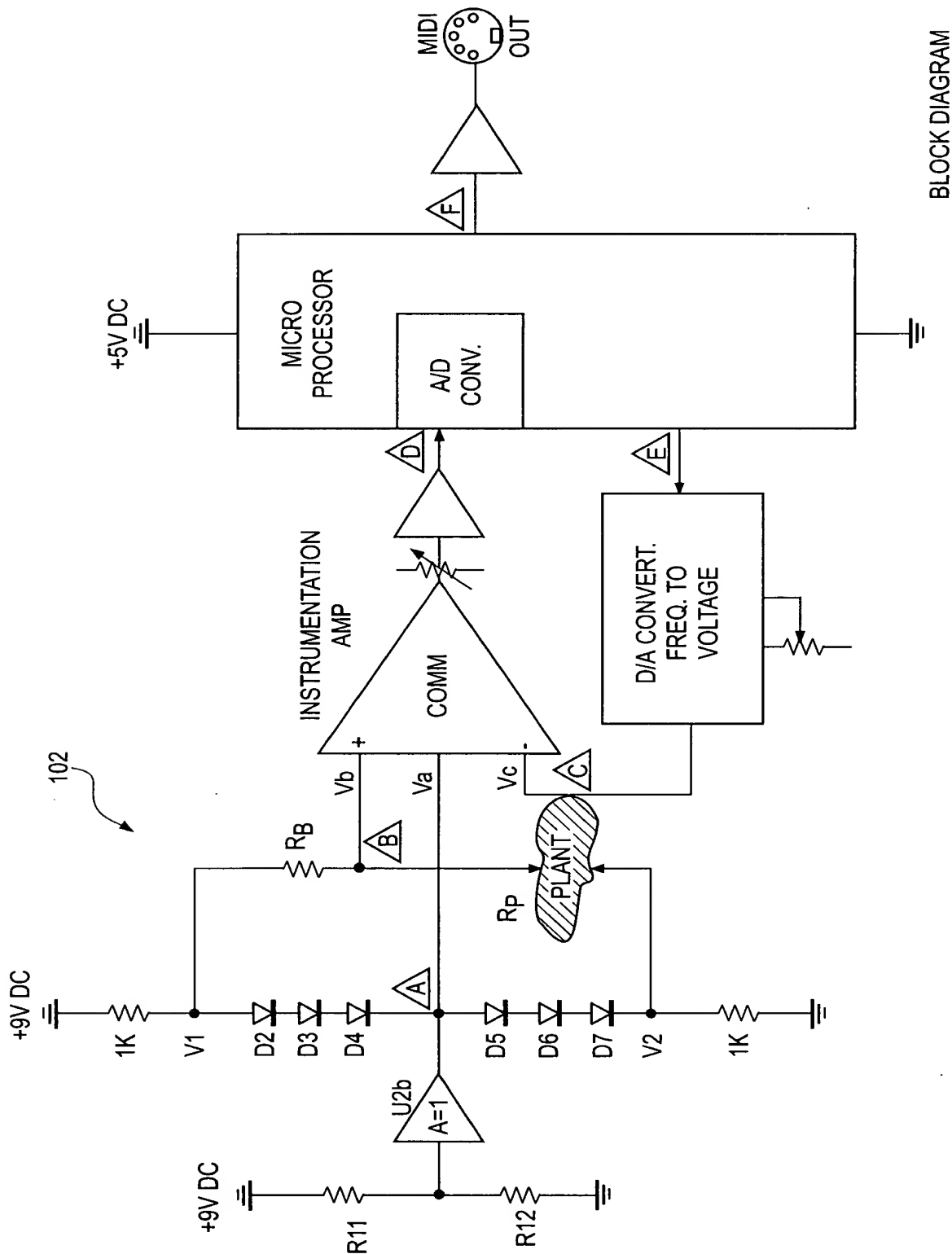
FIG. 7c



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10/10

FIG. 8



BLOCK DIAGRAM